

FIG. 1

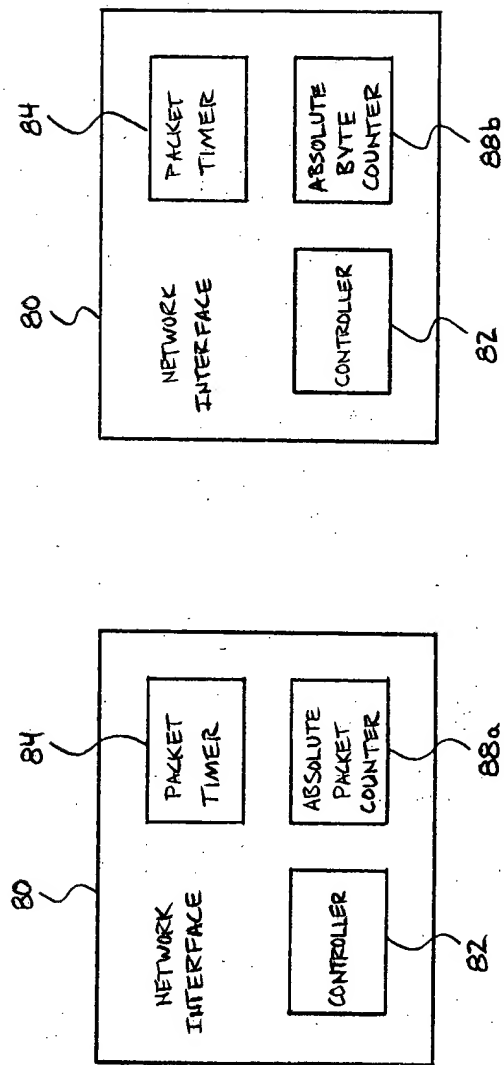


FIG. 2

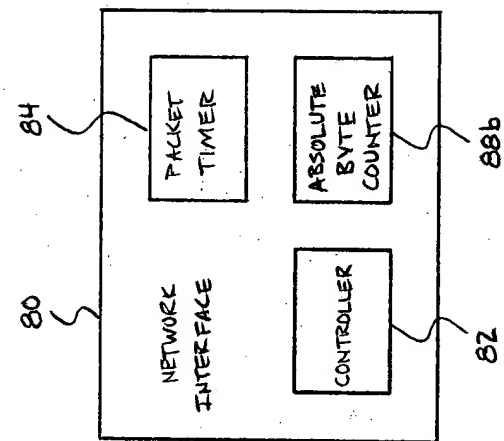
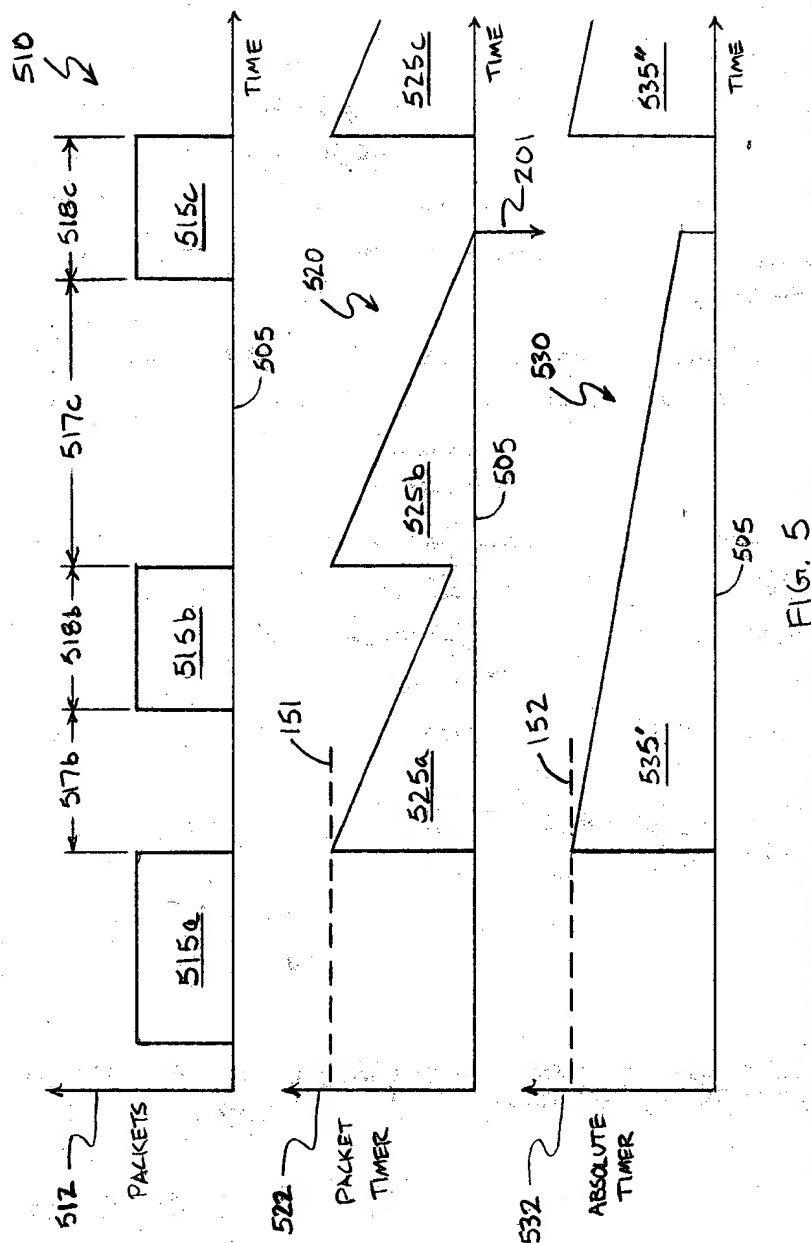


FIG. 3

```

graph TD
    Entry(( )) --> 405{PACKET RECEIVED?}
    405 -- YES --> 410[START PACKET TIMER]
    410 --> 415{IS ABSOLUTE TIMER STARTED?}
    415 -- YES --> 425{HAS A TIMER EXPIRED?}
    415 -- NO --> 420[START ABSOLUTE TIMER]
    420 --> 425
    425 -- YES --> 430[RESET TIMERS]
    430 --> 435[ASSERT INTERRUPT]
    435 --> Entry
    425 -- NO --> Entry

```



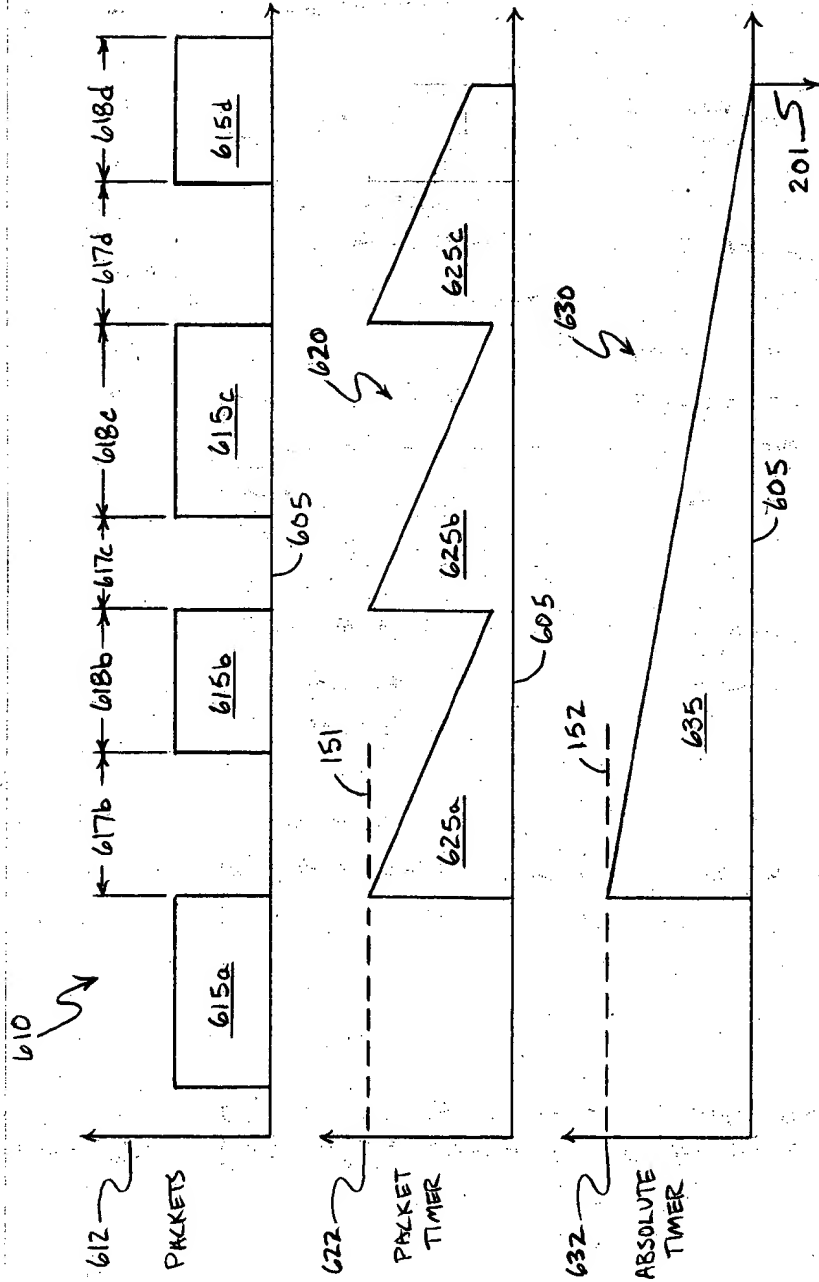


FIG. 6

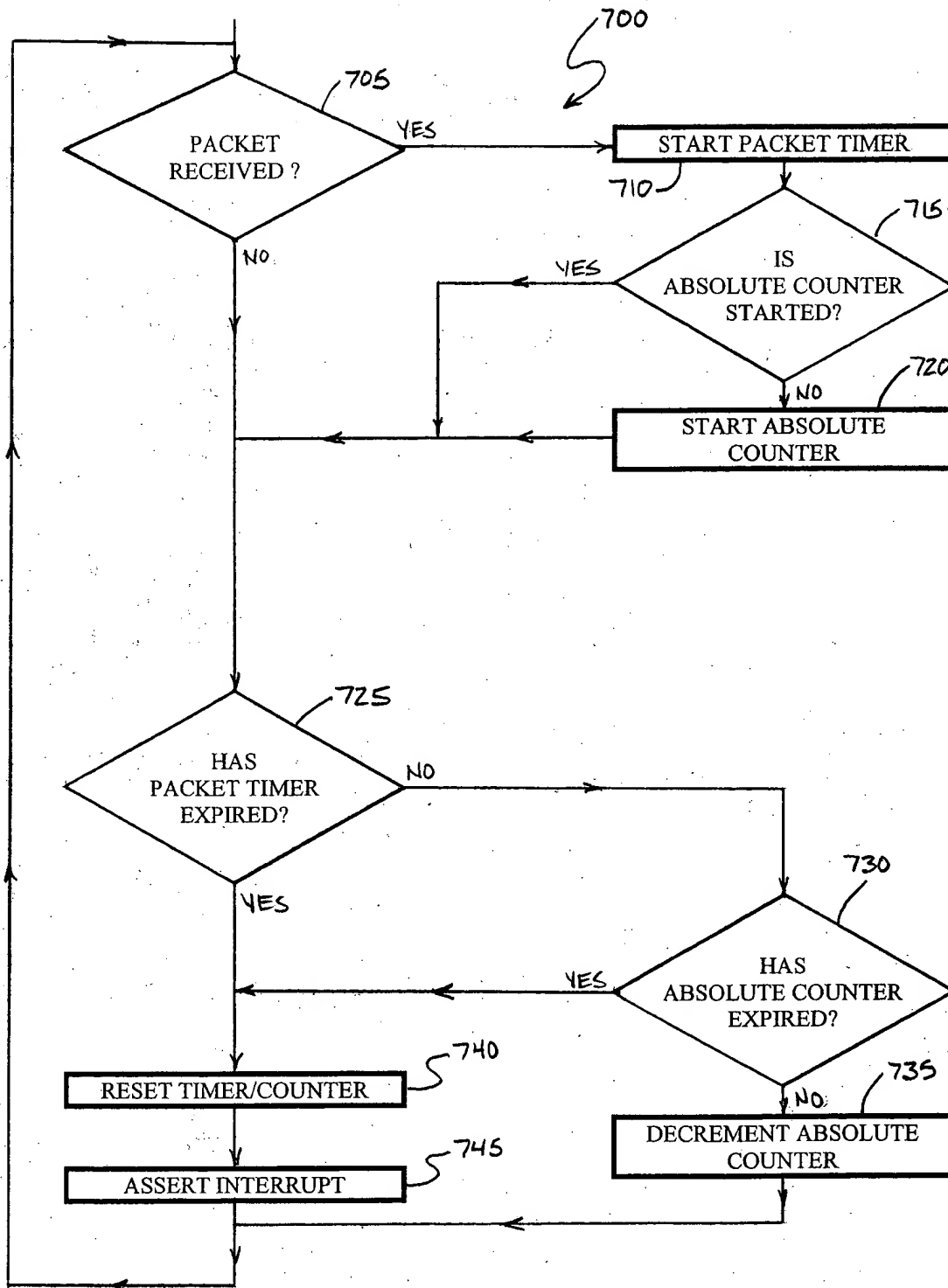


FIG. 7

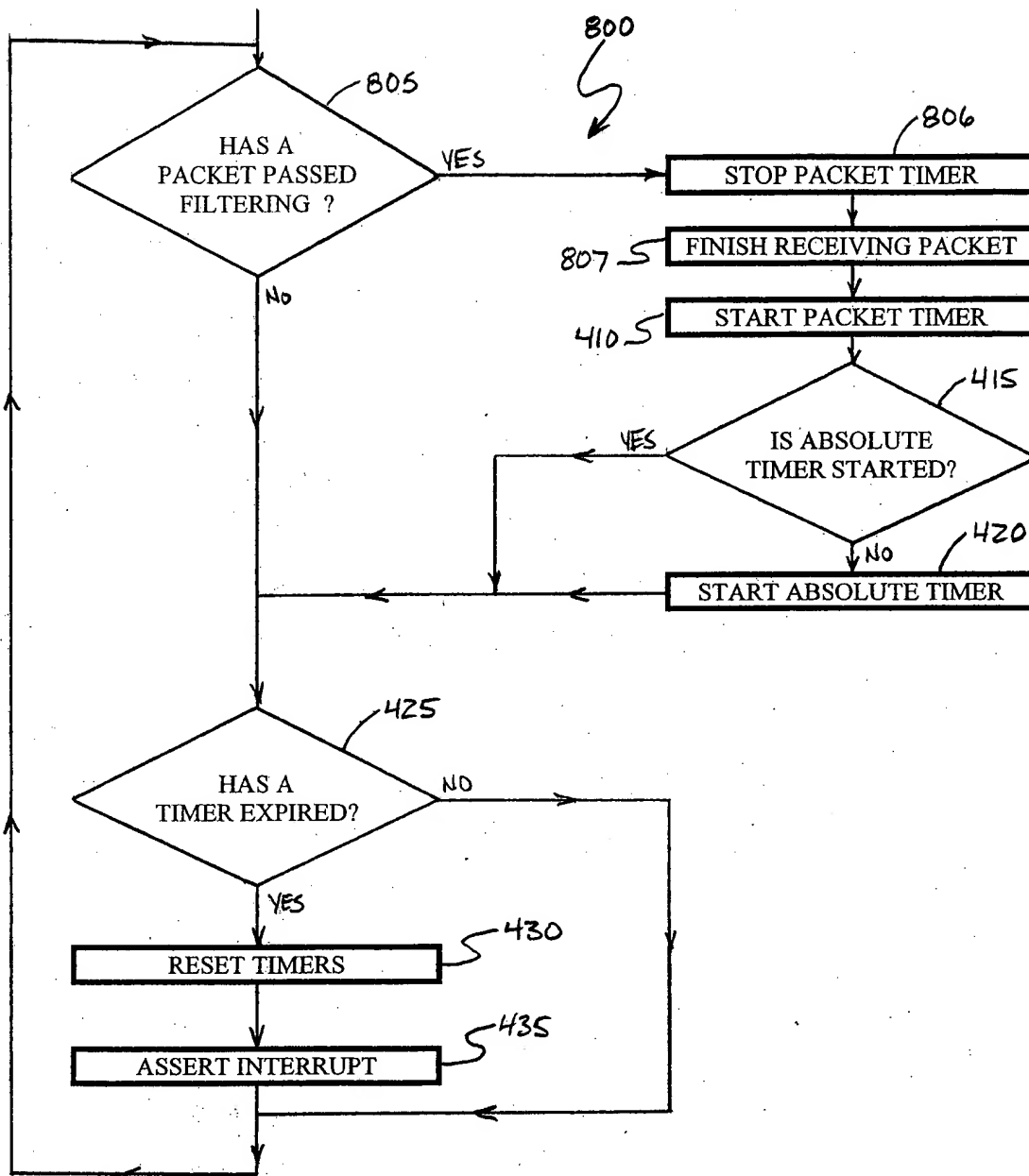


FIG. 8

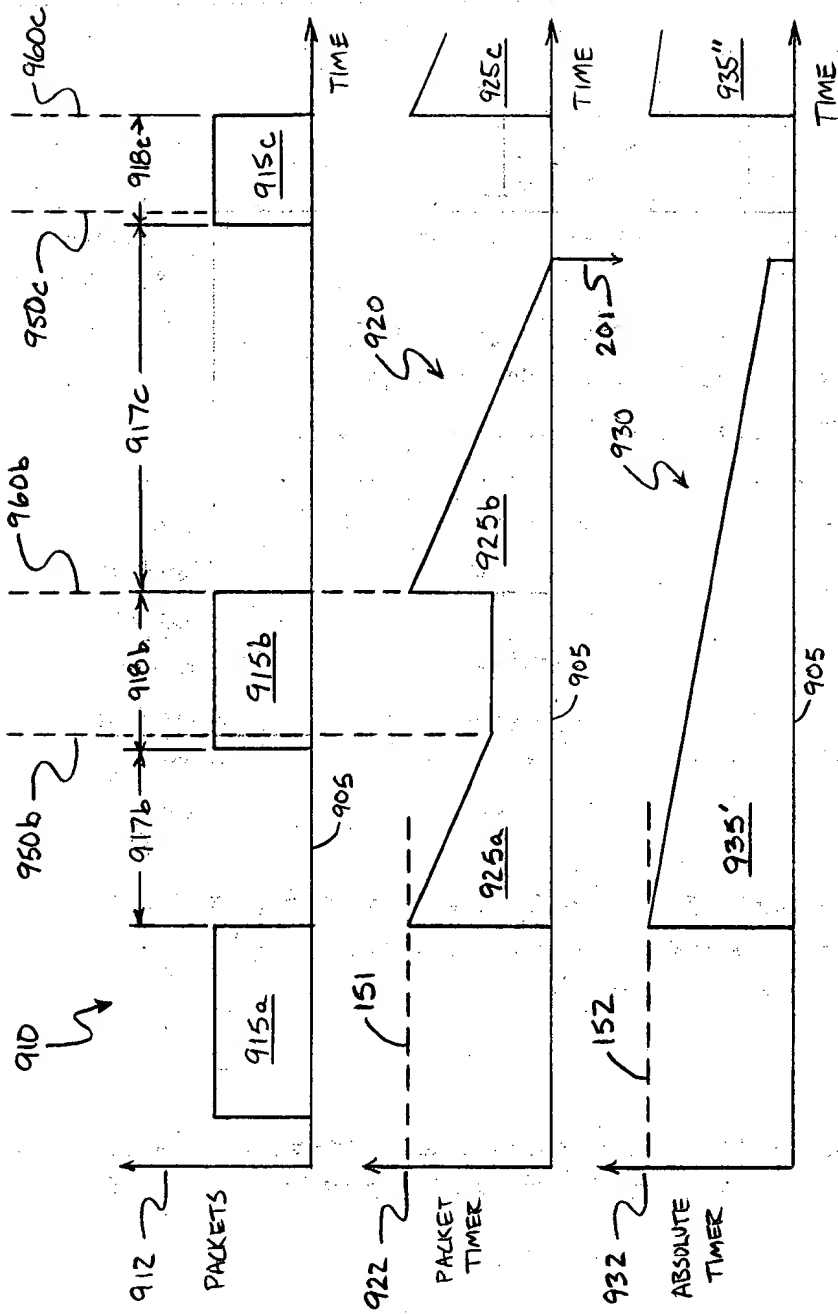


FIG. 9